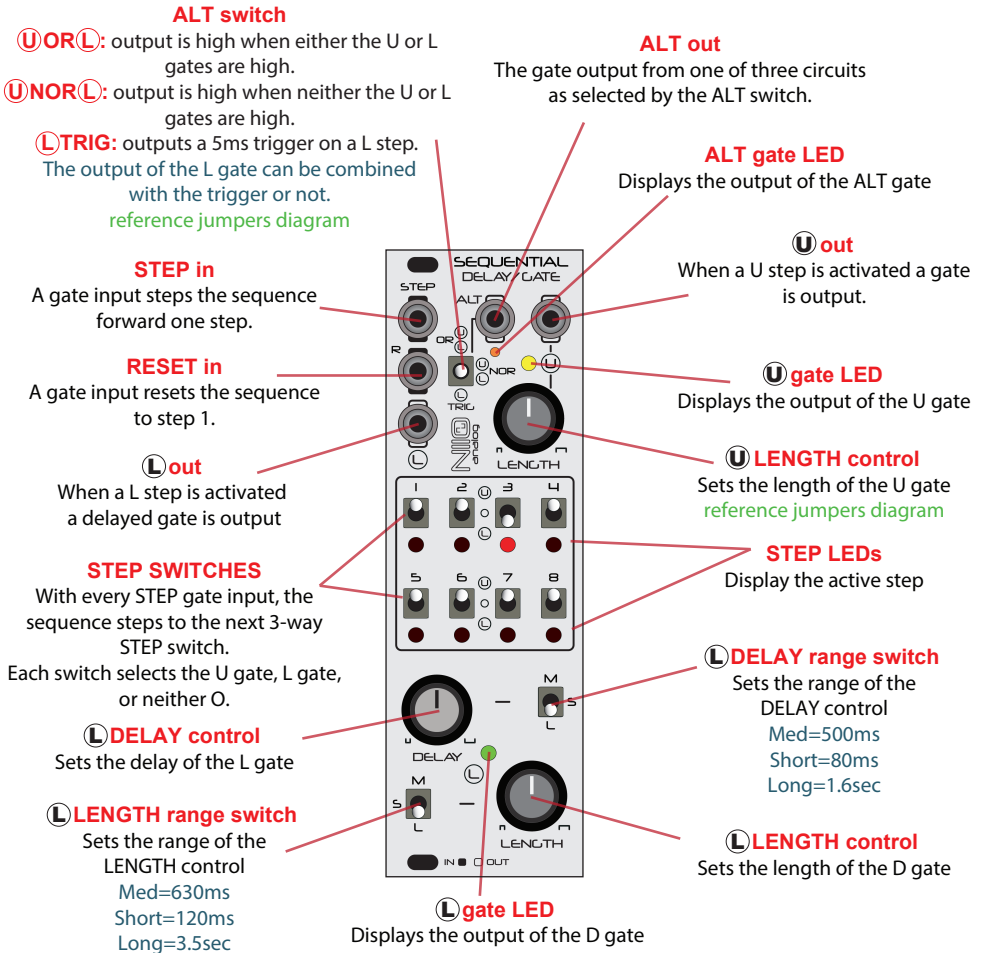


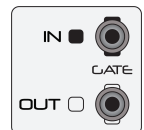
SEQUENTIAL GATE / DELAY GATE

The SEQUENTIAL DELAY GATE is an analog 8 step sequencer that triggers 2 separate gates with adjustable length.

The 2nd gate has delay control, and a third gate that is dependent on the other two. Each step triggers either gate or none.



width: 8hp depth: 25mm 24ma +12v
 niioanalog.com



*Activate gate / CV inputs.

*Gate / trigger envelopes.

*Clock sequencers

STEP

A gate/clock input steps the sequence forward by one step.

RESET (R)

Resets the sequence to step 1 with the leading edge of a gate signal.

STEP SWITCHES

3-position step switches select either the U gate, L gate, or none O for each of the eight steps. red LEDs illuminate on the active step.

U GATE

U is a variable length gate which is set with the LENGTH control.
a YELLOW LED displays the gate.

JUMPER: The 3-PIN jumper on the back sets the maximum time range at 120ms, 630sec, and 3.5sec.

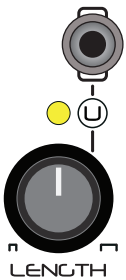
L GATE

L is a variable length delay gate which is set with the DELAY and LENGTH controls.
a GREEN LED displays the gate.

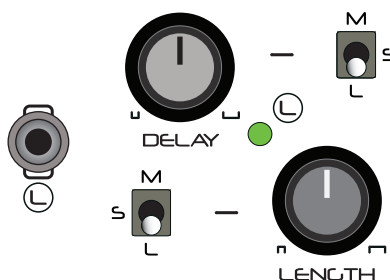
3 usable time ranges for both delay and length allow for easy and precise time setting.

DELAY time range switch select between short(S) 80ms, medium(M) 500ms, and long(L) 1.6sec.

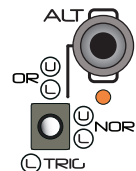
LENGTH time range switch select between short(S) 120ms, medium(M) 630ms, and long(L)3.5sec.



UPPER



LOWER



ALT

ALT OUT

Outputs a gate for one of three conditions as set by the ALT switch.

An ORANGE LED displays the gate.

- Ⓚ OR Ⓛ :output is high when either the U gate or L gate is high.
- Ⓚ NOR Ⓛ :output is high when neither the U gate or L gate is high.

L-TRIG: outputs a 5ms (trigger) at the leading edge of the step when the current step switch is set to Ⓛ. The delayed gate can be included in this output.

TRIG jumper

JUMPER: depending on the back panel (D-DOUBLE TRIG) jumper, either only the step trigger is output, or the step trigger along with the delay gate.

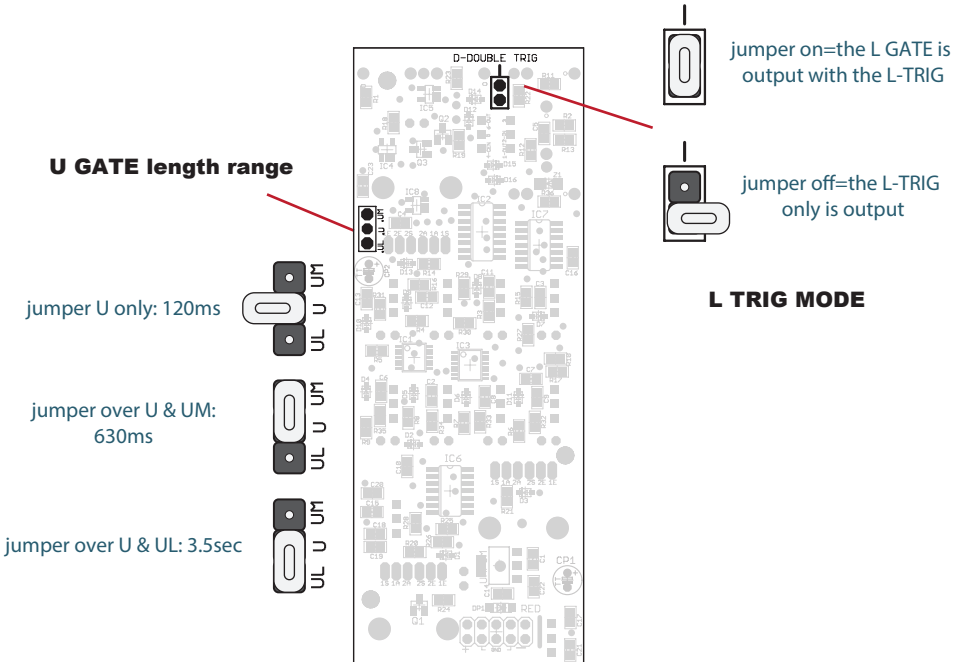
When the back panel jumper is **OFF** only a 5ms trigger is output to the ALT output.

When the jumper is **ON** the step trigger along with the delay gate is output.

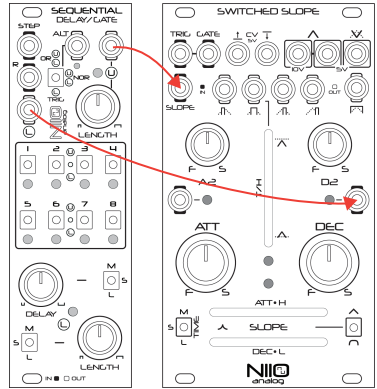
EXAMPLE: To activate an envelope twice with first the non-delayed step, then with the delayed gate.

With the jumper over both pins, patch the ALT out into the GATE input of an envelope. Now every step that selects Ⓛ will double activate the envelope with a 5ms trigger followed with the delayed gate.

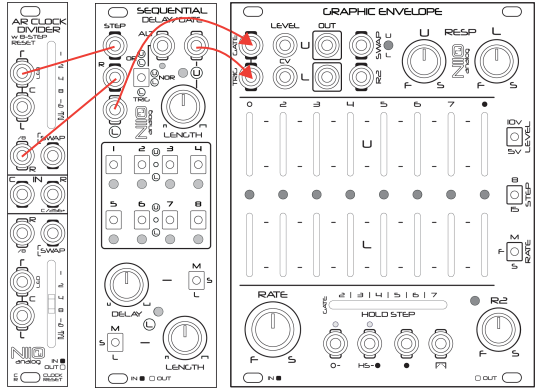
OR leave open to use the ALT output to trigger an envelope, and the Ⓛ output to gate the same or other envelope etc.



Switching gated functions on the SWITCHED SLOPE envelope



Triggering the envelope with the U gate, and gating it with the D gate.



Creating a clock with off-time steps.

