

GATED RANDOM

analog gated CV and GATE random

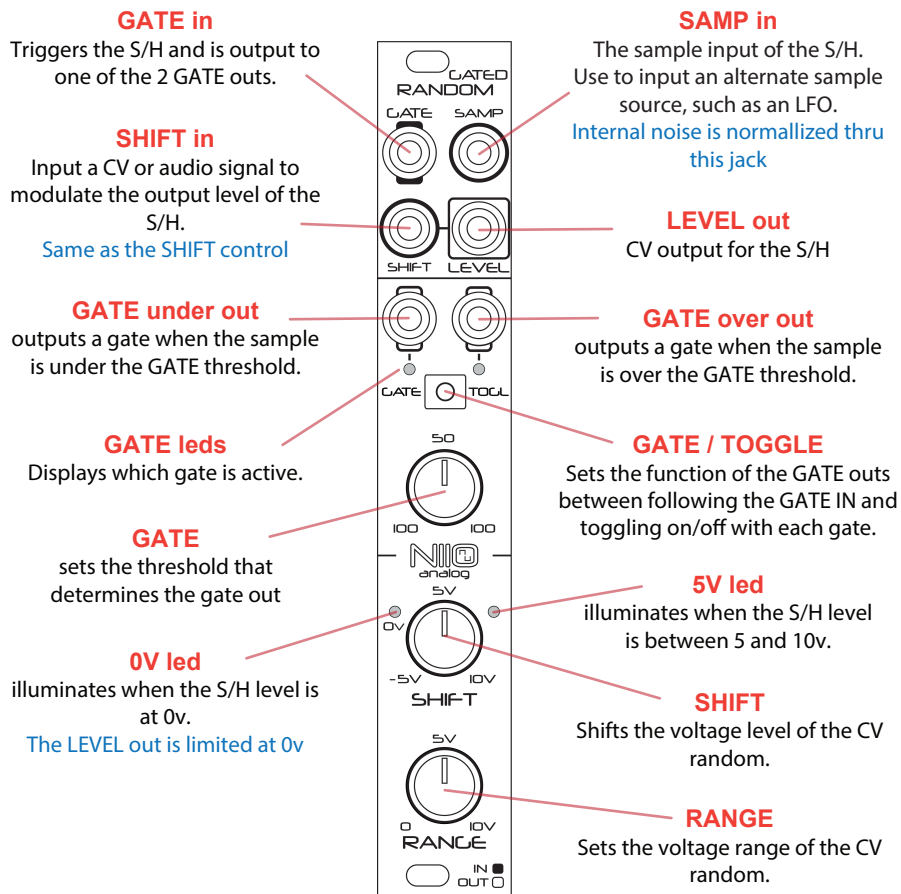
GATED RANDOM outputs a random CV level and thru gate probability with every gate input.

The range of the CV output is adjustable between 0 and 10v.

The level of the CV output can be shifted with the SHIFT control or a CV or audio source.

The GATE in is always passed thru to one of two GATE outs with probability control.

The SAMPLE input allows an external level to be sampled, replacing the internal noise circuit.



width: 4hp depth: 37mm 33ma +12v 19ma -12v

GATE in

The GATE in controls the timing output of the module. Both the CV LEVEL output and the GATE outputs follow the GATE in.

SAMPLE in

An internal noise circuit is normalized thru the SAMP jack. An external signal such as an LFO or envelope can be used to sample from when a GATE in input.

LEVEL out

Every time a GATE is input, a DC level sample from the noise circuit or SAMP is taken. The LEVEL out is the DC level with RANGE and SHIFT affecting its gain and DC shift.

RANGE control

The RANGE control sets the overall level of the LEVEL out. Range is from 0 to 10v and is pre SHIFT.

SHIFT control / SHIFT in

The SHIFT control and CV input add to the level of the LEVEL out. The SHIFT control has a range of -5v to 10v. The LEVEL output is limited at 0v and 10v. Shifting down will shift the entire random signal down beneath 0v, resulting in an increasing amount of the random signal sitting at level 0v allowing for the probability of a random level happening to be adjusted. Shifting up shifts the signal up to and limits it at ~10v. Use SHIFT CV and RANGE to mix random levels with an envelope or sequencer, etc.

GATE control

The GATE control adjusts a threshold level over the sampled signal. When a gate signal is input, the gate is output to one of the 2 GATE outputs depending whether the sampled signal is above or below the threshold.

GATE outs

The GATE outs either follow the length of the GATE in or toggle high/low with every gate that output receives. The 2 position switch determines the function of the gate outs.

